



The Institution of Engineers (India)

TIRUCHIRAPPALLI LOCAL CENTRE

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“100 Years of Relentless Journey towards Engineering Advancement for Nation Building”

Date: 15-11-2019

CIRCULAR

We are pleased to inform you that we have arranged a lecture as per details below:

Topic : **Digital CMOS VLSI design**

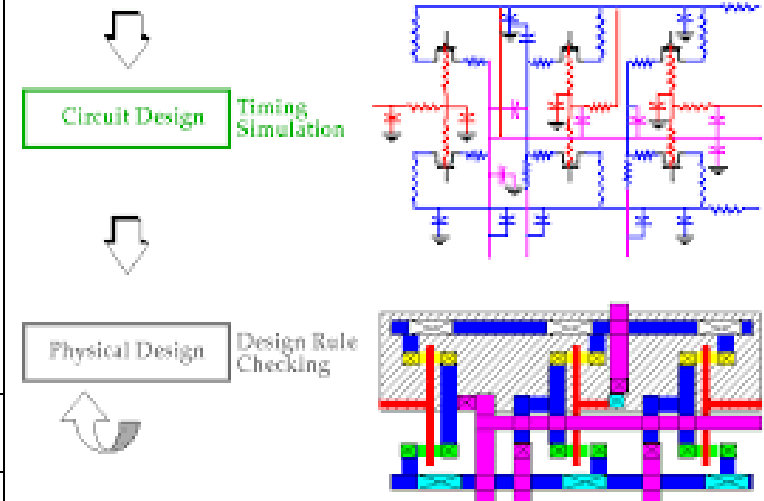
Division : **Electronics and Communication Engineering**

Speaker : **Ms.V.Kavitha,**
Assistant Professor,
M.A.M. College of Engineering and Technology,
Siruganur, Trichy.

Venue : **Institution Building (Opposite to Bldg.79)**

Date : **19th November 2019 (Tuesday)**

Time : **5.00 PM – Tea 5.15 PM - Lecture**



CMOS VLSI Design

We request you to kindly make it convenient to attend the above Lecture programme. **Non-members are also welcome.**

To: **All Members of The Institution of Engineers (India), Tiruchirappalli Local Centre** through Mail / Notice Board.

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